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** CONTINUING DATA ***** *Yes* ***** *IL*

This application is a DIV of 10/101,169 03/20/2002 PAT 6,750,074

** FOREIGN APPLICATIONS ***** *Yes* ***** *IL*JAPAN 2001-322811 *10/19/2001*

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|---|---|-------------------------|----------------------|----------------------------|
| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no | STATE OR COUNTRY JAPAN | SHEETS DRAWING 17 | TOTAL CLAIMS 6 | INDEPENDENT CLAIMS 4 |
| 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance | | | | |
| Verified and Acknowledged | Examiner's Signature <i>[Signature]</i> | Initials <i>IL</i> | | |

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TITLE

Method of Manufacturing a Semiconductor Device and a Method for Fixing the Semiconductor Device Using Substrate Jig